Proposal: Hardware Random Number Generator

(HWRNG)

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A hardware random number generator is an electronic device that produces random numbers based on some physical process. Such a generator has the potential to produce much higher quality random numbers than those produced by the deterministic algorithms typically used in computer programs\(^1\). A hardware random number generator is thus a “true” random number generator as opposed to a pseudo-random number generator.

However, to reach its potential, a hardware random number generator must be designed and implemented carefully or else various sources of bias will corrupt the randomness of its output. There is extensive literature on the subject of hardware random number generator design \([1]\), as well as several commercial products available \([2]\). In addition much has been written on the subject of testing random number generators in general and hardware random number generators in particular \([3]\).

In this project I will construct a simple hardware random number generator and develop a library that will make using the generator from applications easier. The expected application of my generator is to provide seed material for cryptographically strong pseudo-random number generators that are then used to produce keys for encryption algorithms in security sensitive applications \([4]\). A consequence of targeting this particular application domain is that my generator does not need to be fast since the pseudo-random number generator that it seeds can be used to produce most of the output. Instead my generator can focus on producing very high quality random numbers.

To make the generator as useful as possible it will be implemented as a separate unit attached to the computer by way of a standard USB connection. This will allow the hardware to be connected to many different kinds of computers. The application library will provide a uniform interface to the generator. Applications must be written to use the library but, once written, they will be source code portable to all supported systems. Because the command language used by the generator will also be documented, third parties will be able to write their own interface libraries for systems that I do not directly support.

Hardware random number generators are subject to degradation over time, or even outright failure. To ensure the quality of the random numbers produced, my generator will contain a statistical monitoring function. An error will be indicated if the monitor detects insufficient entropy in the generator output.

Because the generator will be used in security sensitive applications, attention will be paid to security issues in the design and implementation of the generator. The application library will be

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\(^1\) A deterministic algorithm can't output truly random numbers at all. If its internal state is known, the output is completely predictable.
carefully reviewed for security problems so that programs that use it are not vulnerable because of flaws in the library. In addition, the generator will be fully documented, following a policy of full disclosure, so that concerned third parties can be confident that it contains no loopholes or backdoors.

Similar Products

A device very similar to my proposed project is TrueRNG by Ubld.it [5]. Like my proposed project it uses a physical process to produce random numbers in a USB connected hardware device. Also TrueRNG supports multiple operating systems (Linux, Windows, OSX) although driver and application support appears a spotty at this time. The TrueRNG web site does provide some data on the randomness of the device's output similar in concept to what I propose doing as well. TrueRNG is available for $49.95 and thus represents a price point for competitive analysis.

Deliverables

The following are the deliverables of this project.

- USB connected hardware device to generate random numbers.
- Driver software for supported operating system(s).
- Interface library providing a suitable API for supported language(s).
- Complete documentation including source code and schematics.
- Results of randomness testing and security review.

References


4: D. Eastlake, J. Schiller, S. Crocker, RFC-4086; Randomness Requirements for Security, 2005